//---------------------------------------CONFIG PORT E FOR INTERRUPT---------------------------------

void configPortE(void) // PE(0):in

{int waitcycle=0;

SYSCTL\_RCGC2\_R = (1<<4); // activate PORTE clock

waitCycle++;

GPIO\_PORTE\_DEN\_R |= 0x0001; // enable pins E(0)

GPIO\_PORTE\_DIR\_R |= 0x0000; //set to input

GPIO\_PORTE\_IS\_R &= ~0x01; // interrupt sense to "edge-sensitive"

GPIO\_PORTE\_IBE\_R &= ~0x01; // interrupt trigger set to "single edge"

GPIO\_PORTE\_IEV\_R |= 0x01; // interrupt event to rising edge

GPIO\_PORTE\_ICR\_R |= 0x01; // clear interrupt PORTE(0)

GPIO\_PORTE\_IM\_R |= 0x01; // unmask PORTE(0) = S1

NVIC\_EN0\_R |= (1<<4); // enable PortE interrupt (Int#4/Vec#20) in NVIC

}